



500.40530X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Y. OSONE, et al.  
Serial No.: 09 / 943,512  
Filed: AUGUST 31, 2001  
Title: MULTILAYER WIRING BOARD AND SEMICONDUCTOR  
DEVICE.

LETTER CLAIMING RIGHT OF PRIORITY

Assistant Commissioner for  
Patents  
Washington, D.C. 20231

SEPTEMBER 25, 2001

Sir:

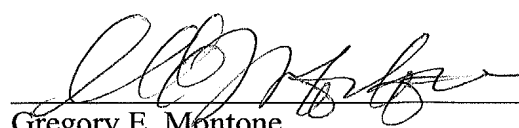
Under the provisions of 35 USC 119 and 37 CFR 1.55, the applicant(s) hereby claim(s)  
the right of priority based on:

Japanese Patent Application No. 2001 - 150260  
Filed: MAY 21, 2001

A certified copy of said Japanese Patent Application is attached.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

  
\_\_\_\_\_  
Gregory E. Montone  
Registration No. 28,141

GEM/rp  
Attachment

## ARTIFACT SHEET

Indicate quantity of item(s) received by the USPTO but not scanned.

☒ Priority Document No. 2001-150260 Country JP  
Doc Code: FRPR

☐ CD(s) containing computer program listing  
Doc Code: Computer

☐ Stapled Set(s) of Extra Color Drawings  
Doc Code: Artifact

☐ CD(s) containing pages of specification and/or sequence listing ☐  
Doc Code: Artifact

☐ CD(s) with content unspecified  
Doc Code: Artifact

☐ Microfilm(s)  
Doc Code: Artifact

☐ Video tape(s)  
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☐ Other mass storage media, description: \_\_\_\_\_  
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☐ Other, description: \_\_\_\_\_  
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